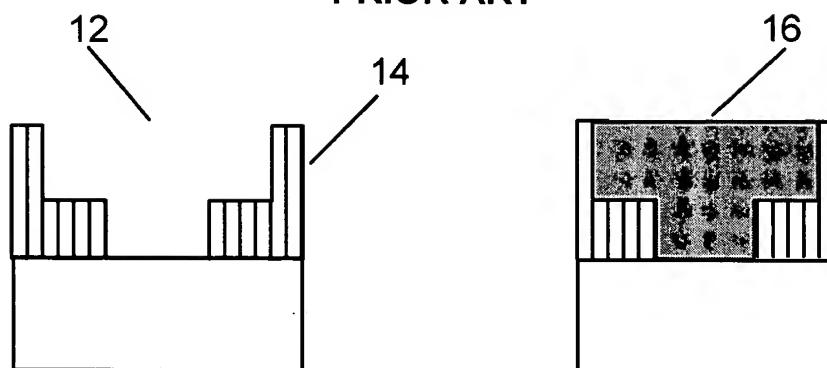
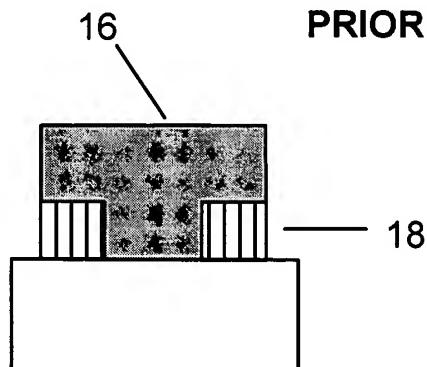


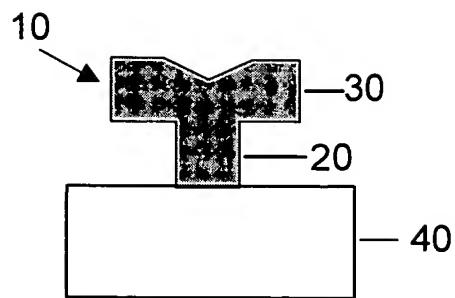
**FIG 1**  
**PRIOR ART**



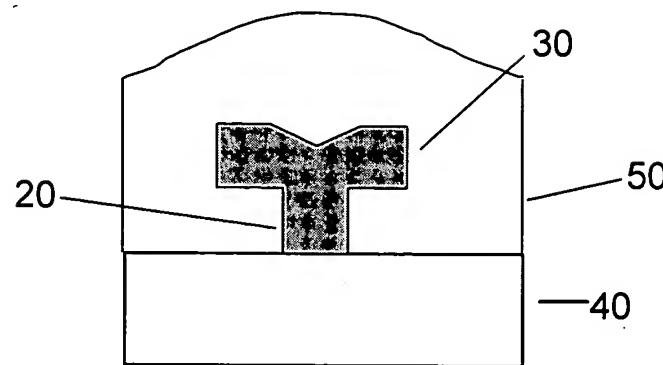
**FIG 2A**                                   **FIG 2B**  
**PRIOR ART**                                   **PRIOR ART**



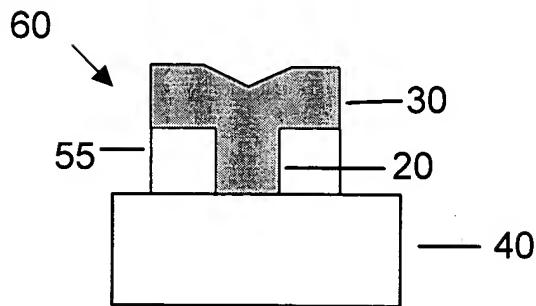
**FIG 2C**  
**PRIOR ART**



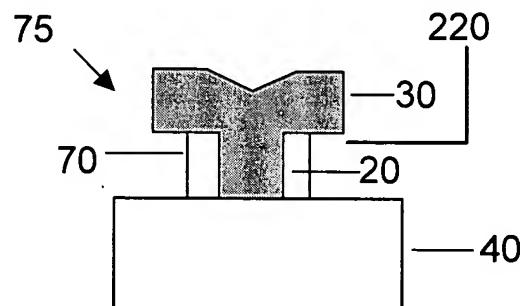
**FIG 3A**



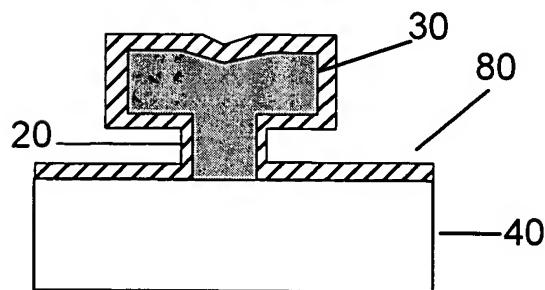
**FIG 3B**



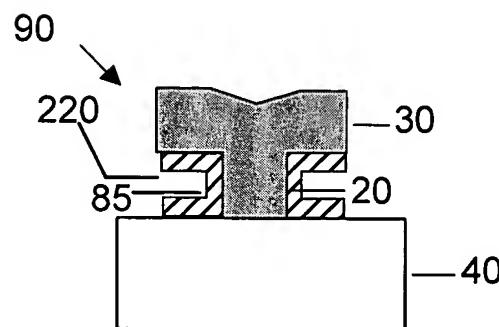
**FIG 3C**



**FIG 3D**



**FIG 3E**



**FIG 3F**

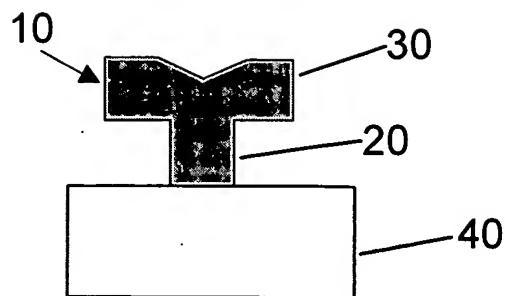


FIG 4A

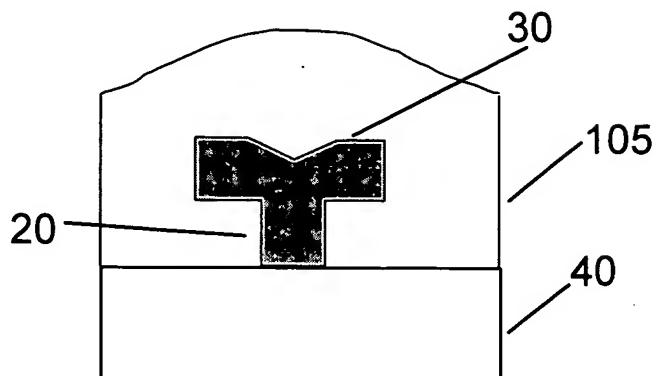


FIG 4B

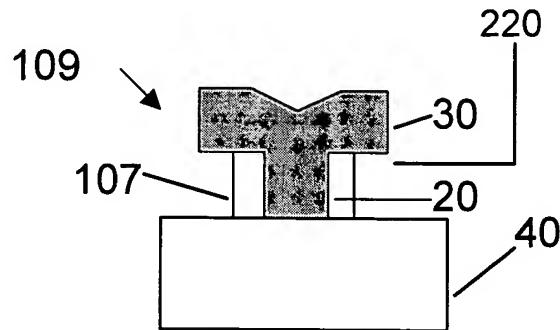


FIG 4C

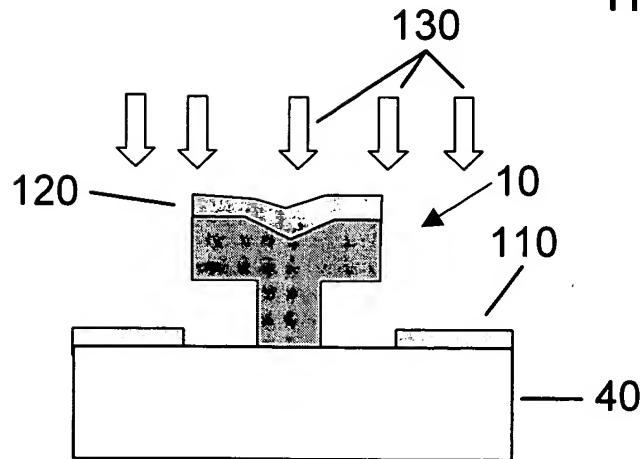


FIG 5A

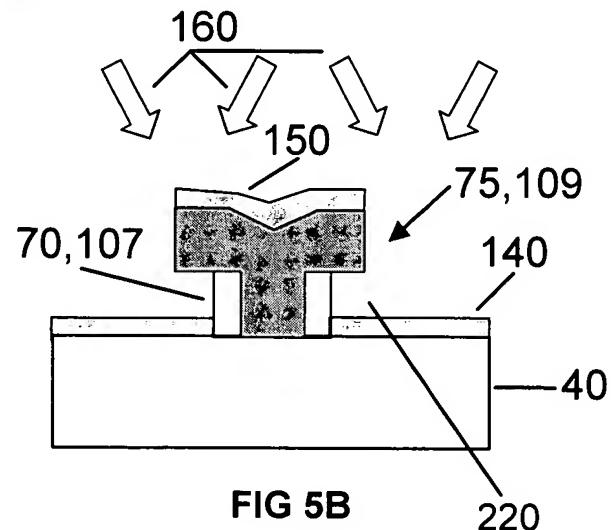
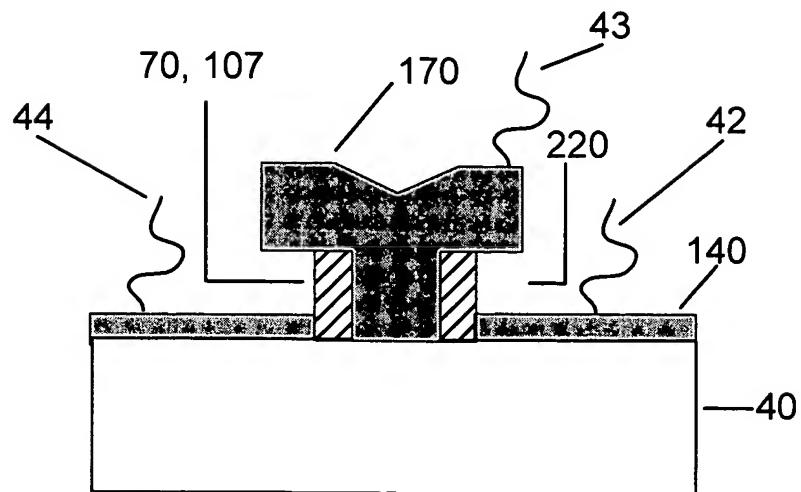
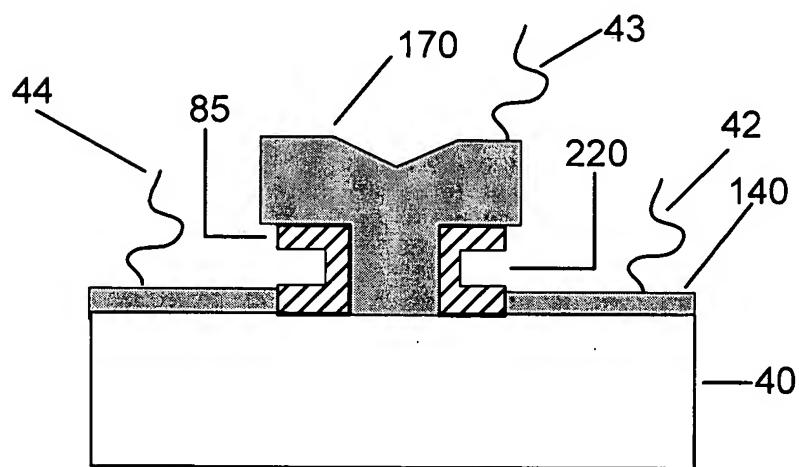


FIG 5B

PRIOR ART



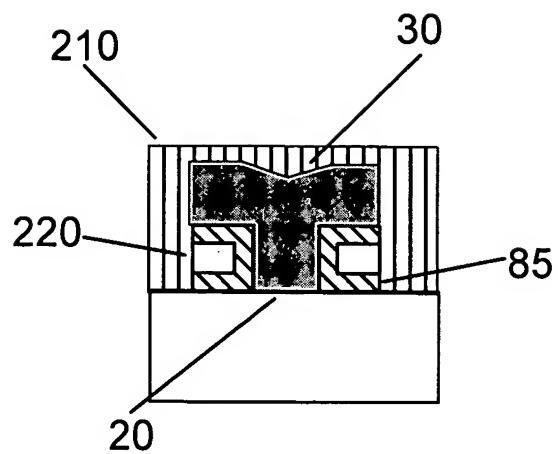
**FIG. 6A**



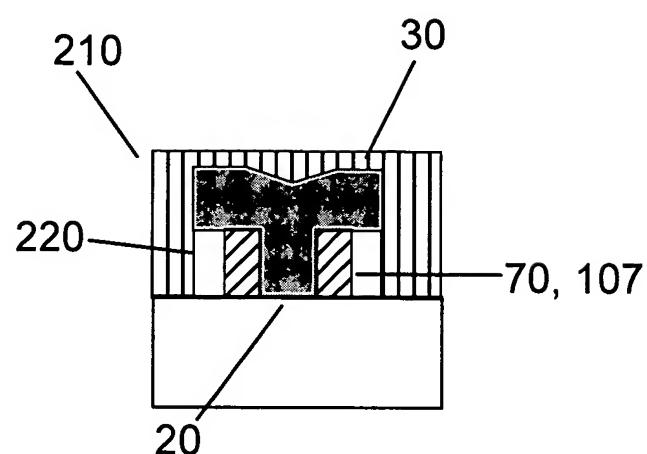
**FIG. 6B**

Integrated circuit  
comprising  
Enhanced T-gate MODFET

**FIG. 7**



**FIG 8A**



**FIG 8B**